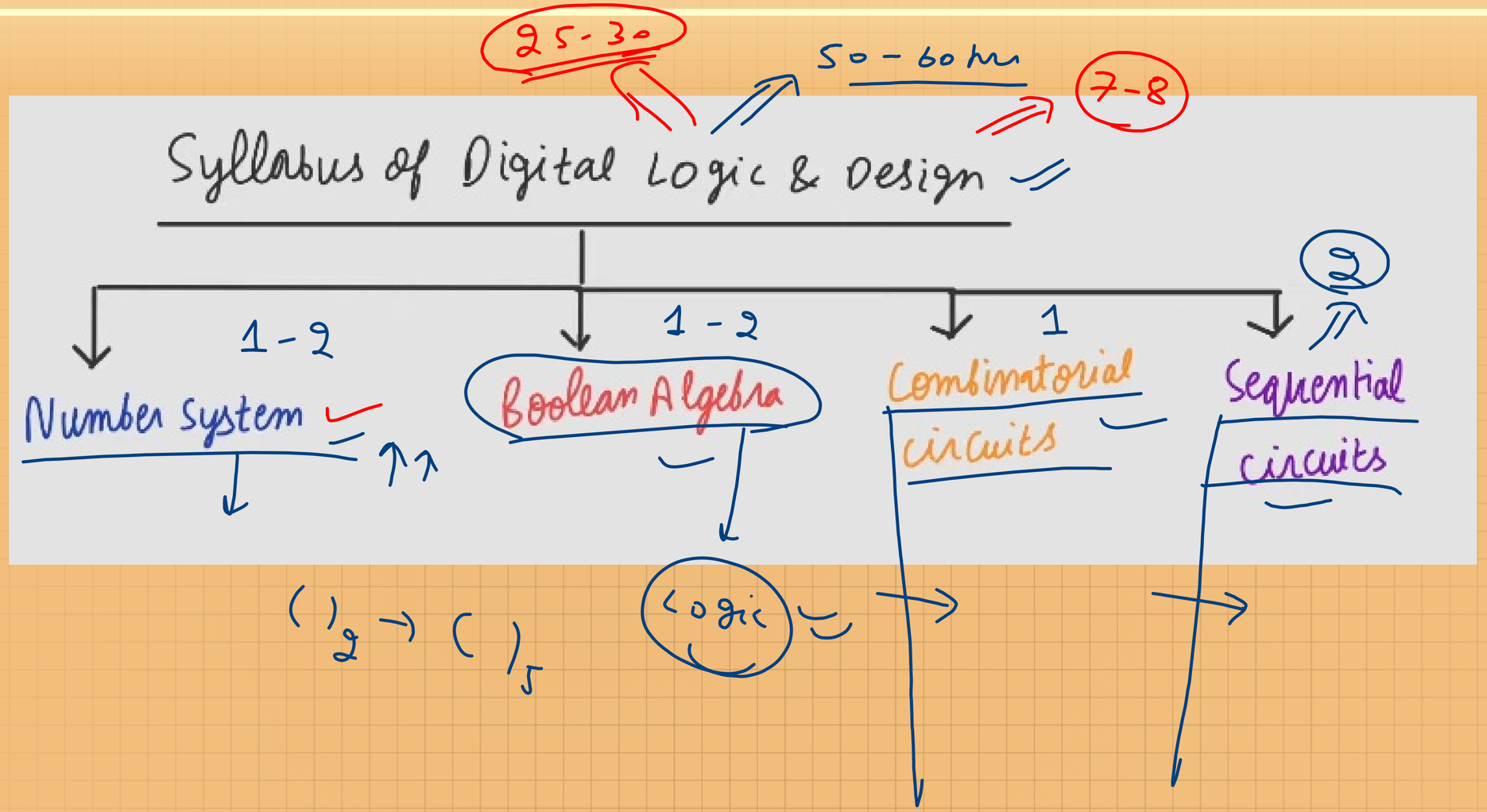

25-30
50-60 hrs
7-8
Syllabus of Digital Logic & Design
1-2
1-2
1
2
Number System
Boolean Algebra
Combinatorial circuits
Sequential circuits
Logic
( )2 → ( )5

25-30

50-60 hrs

7-8

Syllabus of Digital Logic & Design

- Number System — 1-2
- Boolean Algebra — 1-2
- Combinatorial circuits — 1
- Sequential circuits — 2

$( )_2 \rightarrow ( )_5$

Logic

Recorded

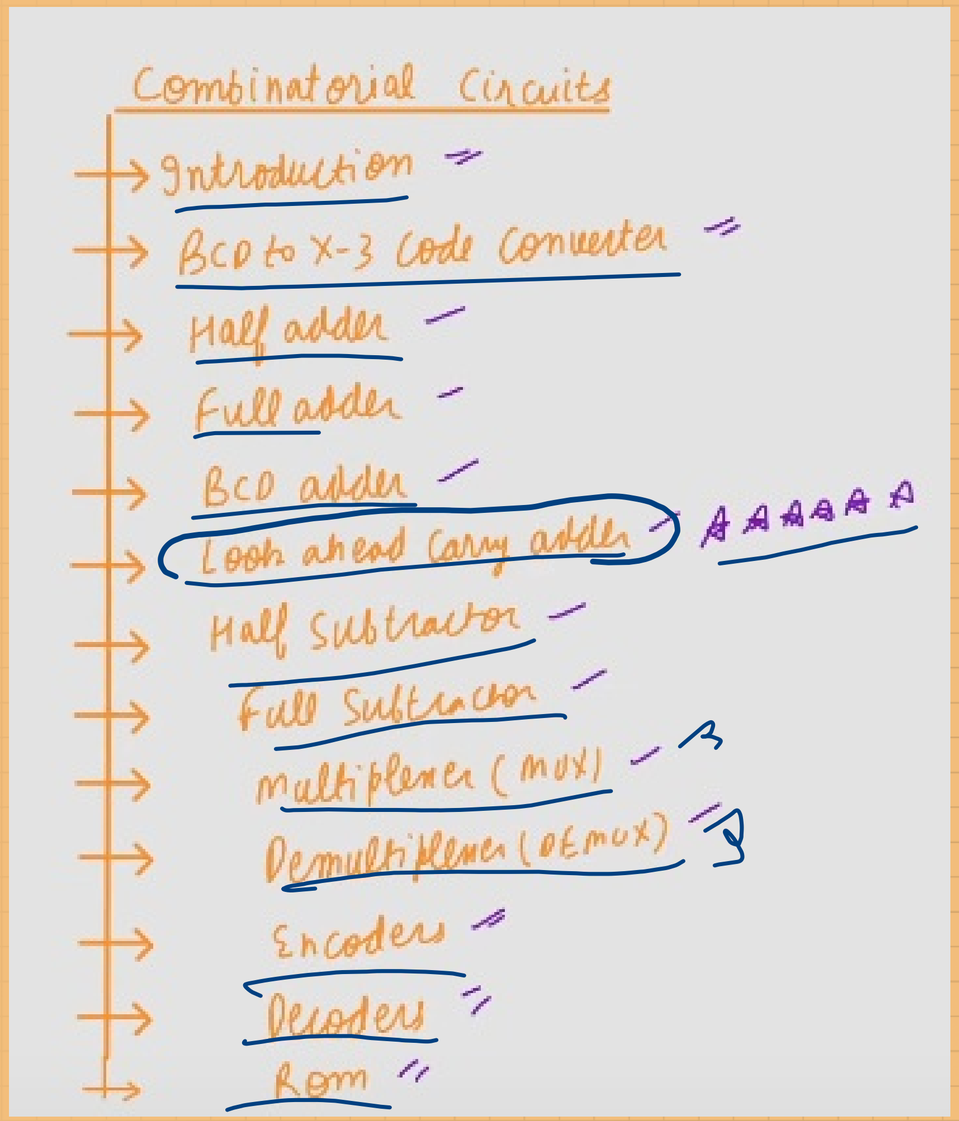
Dead

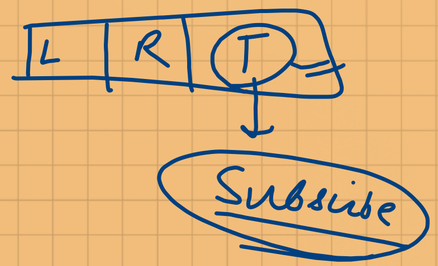
Number System

- Introduction ✓
- Conversion ✓ $()_2 \rightarrow ( )_5$ $(1011)_2 \rightarrow ( )_5$
- Arithmetic operations → +, *, -, /
- Compliments
- Codes
  - BCD
  - X-3
  - Self Complimenting Codes
  - Grey code
  - Alphanumeric codes
- Representation of Numbers in Computers
  - Integers
  - Real No.
    - IEEE

Boolean Algebra

- Introduction
- Boolean Laws
- Logic Gates
- Principle of Duality
- 2 ways to represent Boolean Functions
  - SOP
  - POS
- Kmaps


Combinatorial Circuits
Introduction
BCD to X-3 Code Converter
Half adder
Full adder
BCD adder
Look ahead Carry adder
Half Subtractor
Full Subtractor
multiplexer (MUX)
Demultiplexer (DEMUX)
Encoders
Decoders
Rom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Sequential circuits

- Introduction
- Latch
- SR Flip flop
- JK Flip flop
- Master slave flip flop
- Counters
- Registers
- Booth's algorithm for multiplication